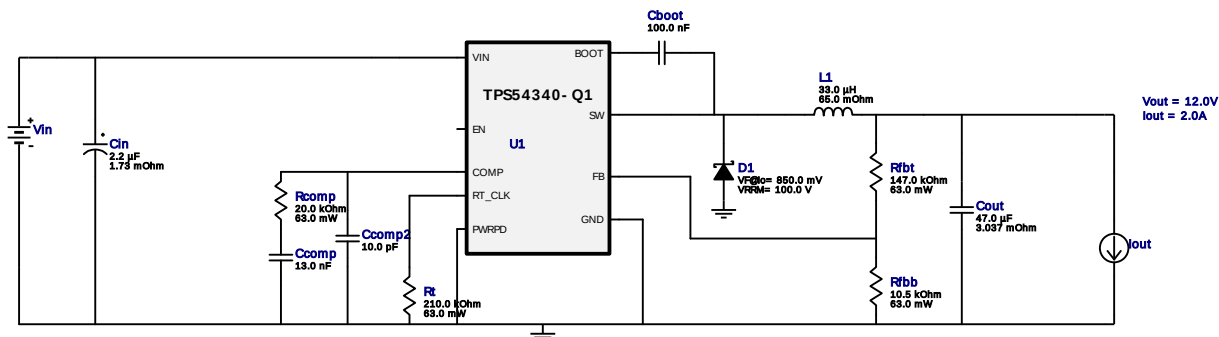


WEBENCH® Design Report

Design : 4077713/45 TPS54340QDDARQ1
TPS54340QDDARQ1 36.0V-38.0V to 12.00V @ 2.0A



1. This regulator device is qualified for Automotive applications. All passives and other components selected in this design may not be qualified for Automotive applications. The user is required to verify that all components in the design meet the qualification and safety requirements for their specific application. View WEBENCH(R) Disclaimer.

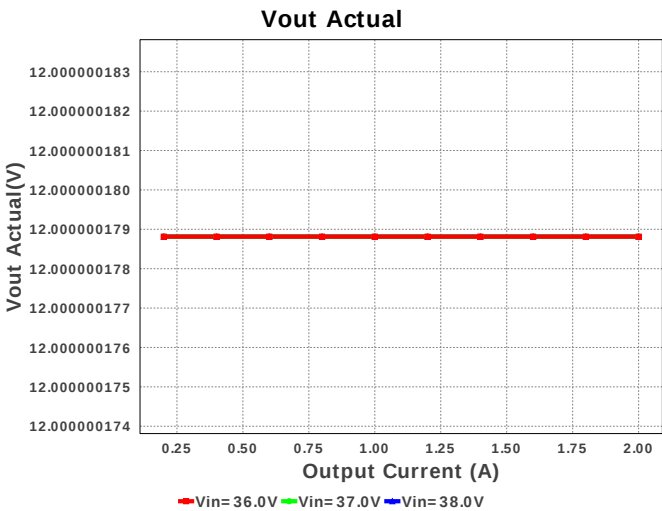
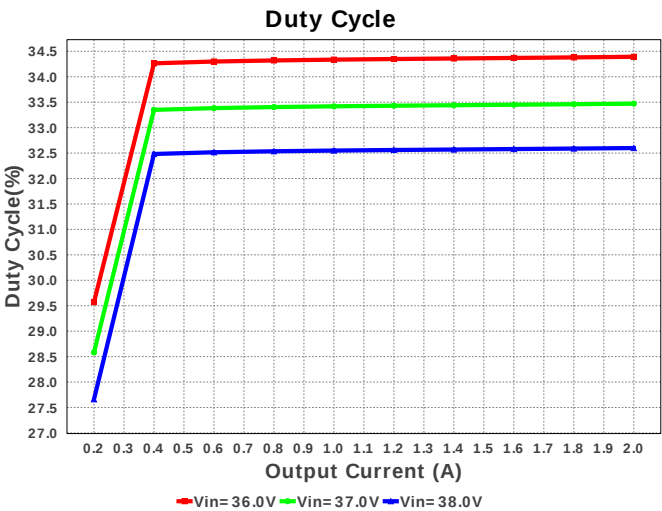
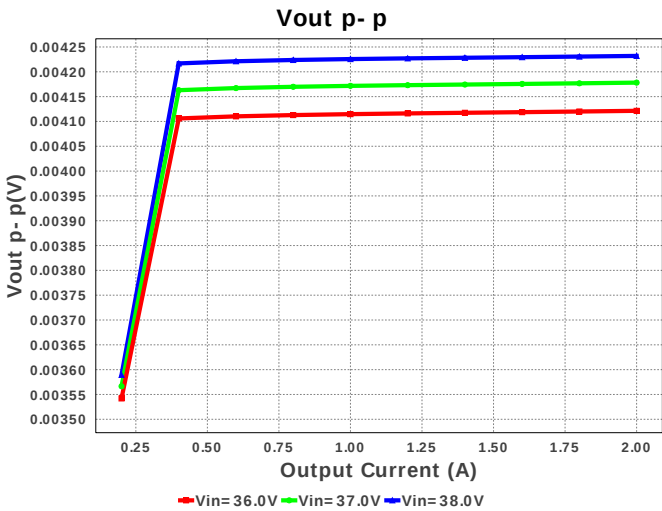
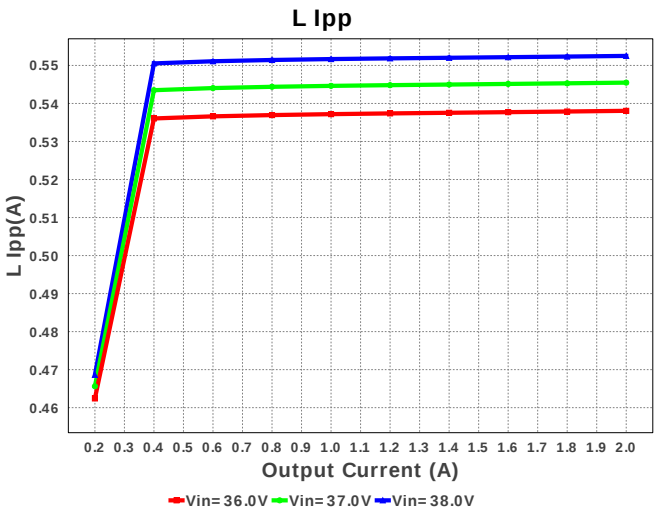
Electrical BOM

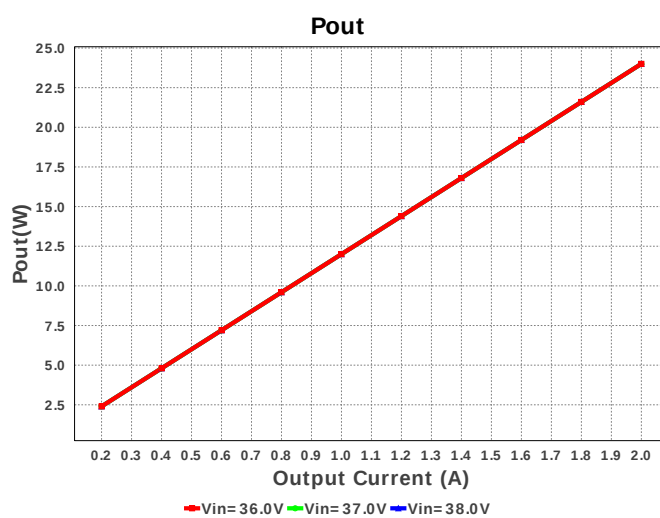
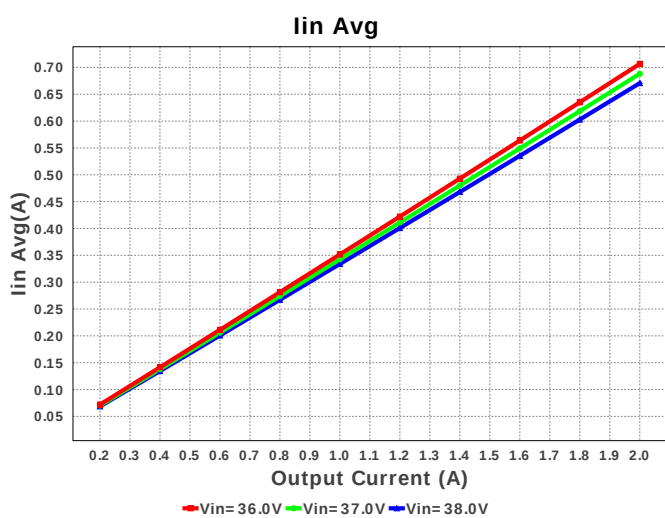
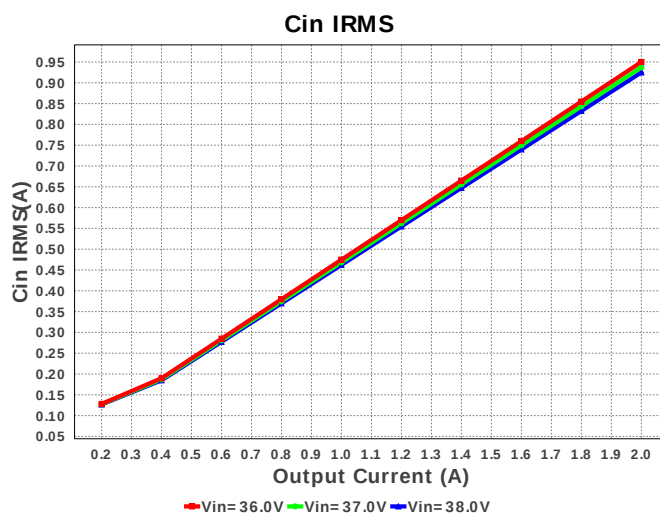
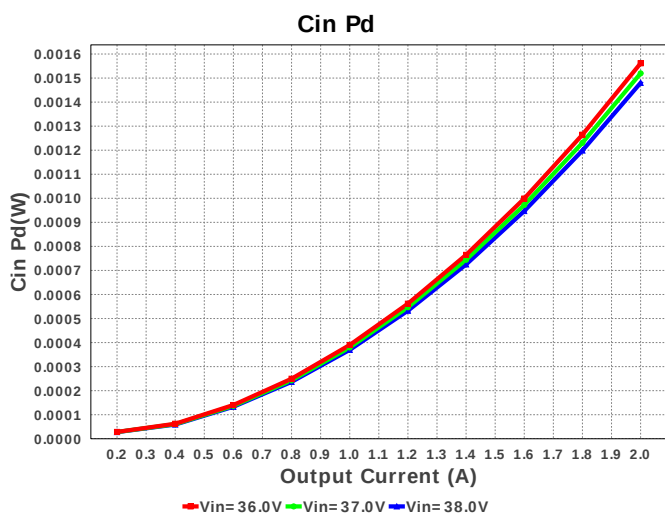
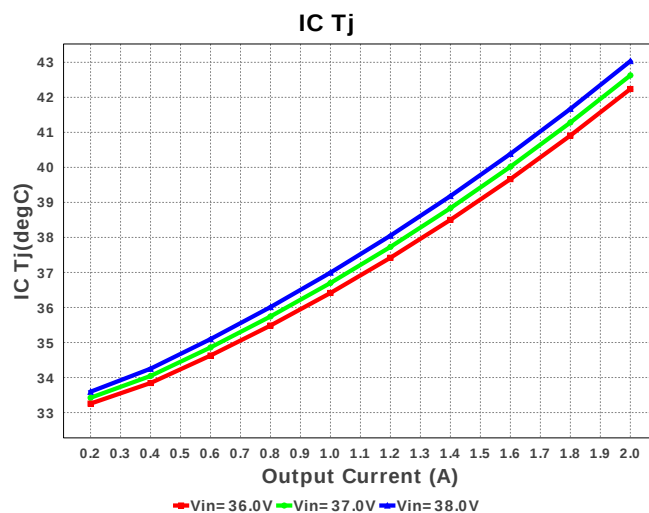
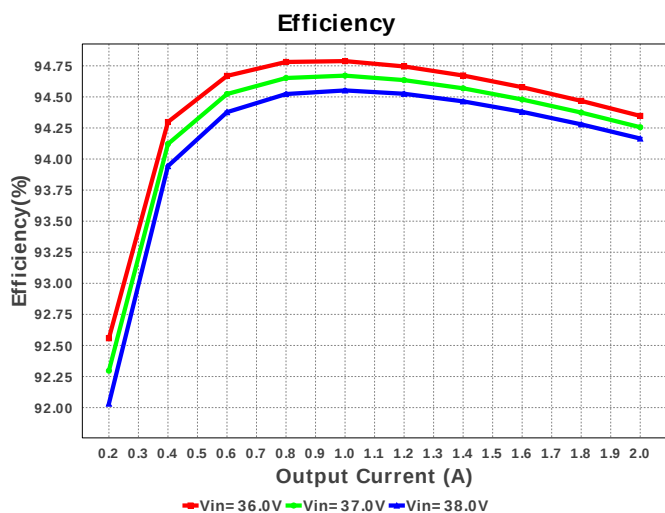
#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	Cboot	MuRata	GRM155R61A104KA01D Series= X5R	Cap= 100.0 nF VDC= 10.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
2.	Ccomp	MuRata	GRM3195C1H133JA01D Series= C0G/NP0	Cap= 13.0 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.07	1206 11 mm ²
3.	Ccomp2	Kemet	C0805C100K5GACTU Series= C0G/NP0	Cap= 10.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm ²
4.	Cin	TDK	C3225X7R2A225K230AB Series= X7R	Cap= 2.2 uF ESR= 1.73 mOhm VDC= 100.0 V IRMS= 5.5932 A	1	\$0.19	1210_250 15 mm ²
5.	Cout	MuRata	GRM32ER61C476ME15L Series= X5R	Cap= 47.0 uF ESR= 3.037 mOhm VDC= 16.0 V IRMS= 4.59346 A	1	\$0.24	1210_280 15 mm ²
6.	D1	Micro Commercial Components	SK310A-TP	VF@Io= 850.0 mV VRRM= 100.0 V	1	\$0.10	SMA 37 mm ²
7.	L1	Bourns	SDR1307-330KL	L= 33.0 uH DCR= 65.0 mOhm	1	\$0.35	SDR1307 227 mm ²
8.	Rcomp	Vishay-Dale	CRCW040220K0FKED Series= CRCW..e3	Res= 20.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
9.	Rfbb	Vishay-Dale	CRCW040210K5FKED Series= CRCW..e3	Res= 10.5 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
10.	Rf1	Vishay-Dale	CRCW0402147KFKED Series= CRCW..e3	Res= 147.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²

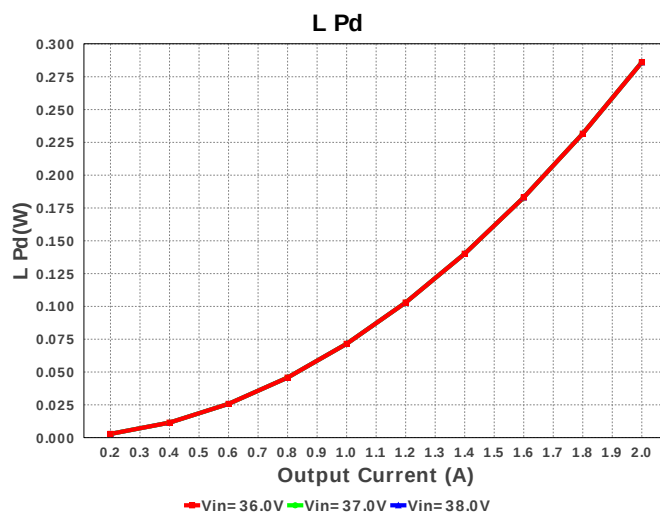
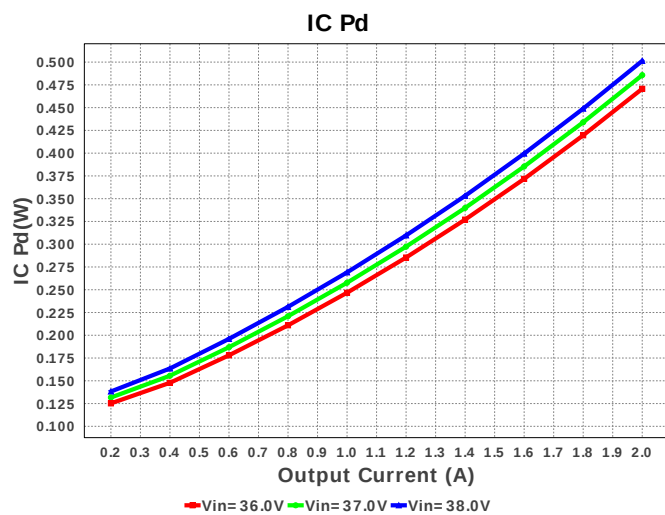
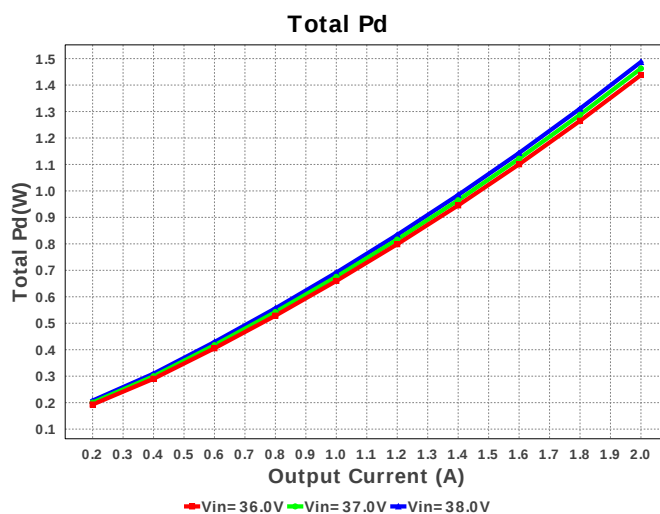
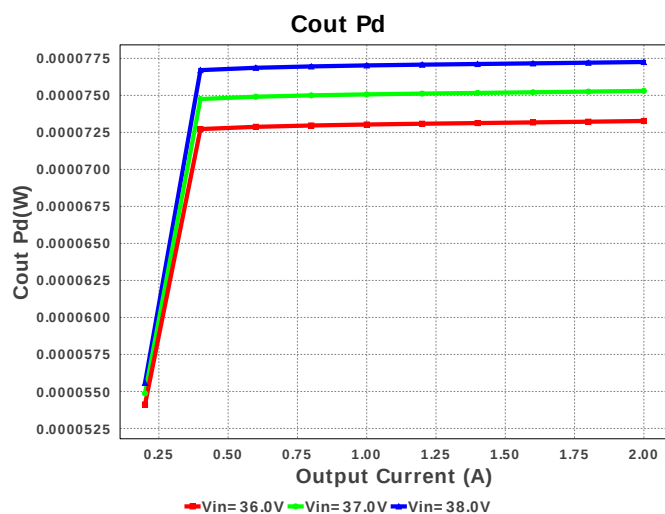
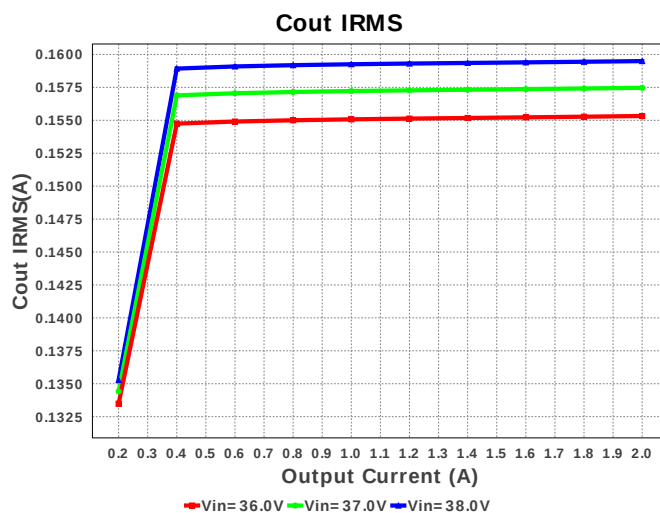
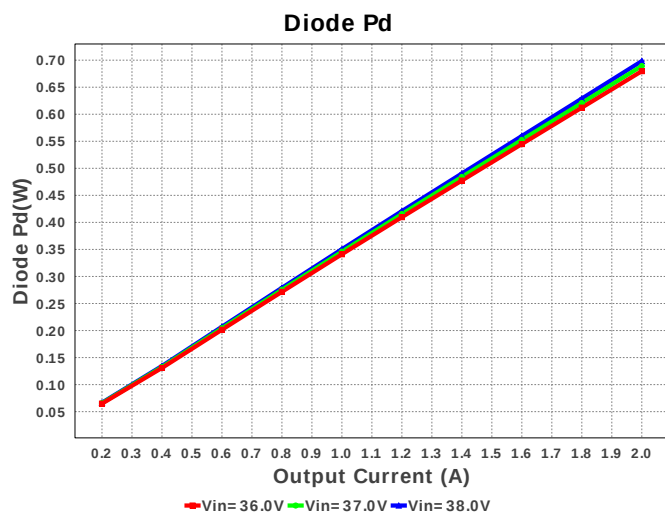
#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
11.	Rt	Vishay-Dale	CRCW0402210KFKED Series= CRCW..e3	Res= 210.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²
12.	U1	Texas Instruments	TPS54340QDDARQ1	Switcher	1	\$2.00	

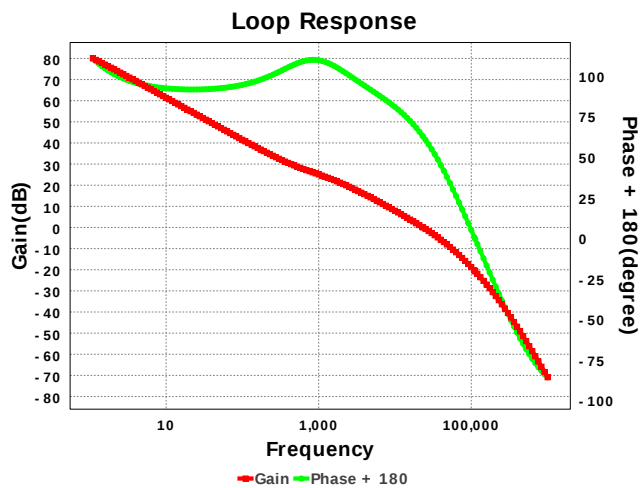


R-PDSO-G8 57 mm²









Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	928.883 mA	Current	Input capacitor RMS ripple current
2.	Cout IRMS	162.326 mA	Current	Output capacitor RMS ripple current
3.	Iin Avg	682.3 mA	Current	Average input current
4.	L Ipp	562.31 mA	Current	Peak-to-peak inductor ripple current
5.	BOM Count	12	General	Total Design BOM count
6.	FootPrint	383.0 mm ²	General	Total Foot Print Area of BOM components
7.	Frequency	464.858 kHz	General	Switching frequency
8.	Pout	24.0 W	General	Total output power
9.	Total BOM	\$3.01	General	Total BOM Cost
10.	ICThetaJA Effective	26.0 degC/W	Op_Point	Effective IC Junction-to-Ambient Thermal Resistance
11.	Low Freq Gain	80.0 dB	Op_Point	Gain at 10Hz
12.	Vout Actual	12.0 V	Op_Point	Vout Actual calculated based on selected voltage divider resistors
13.	Vout OP	12.0 V	Op_Point	Operational Output Voltage
14.	Cross Freq	23.369 kHz	Op_point	Bode plot crossover frequency
15.	Duty Cycle	33.177 %	Op_point	Duty cycle
16.	Efficiency	92.566 %	Op_point	Steady state efficiency
17.	Gain Marg	-20.797 dB	Op_point	Bode Plot Gain Margin
18.	IC Tj	43.098 degC	Op_point	IC junction temperature
19.	IOUT_OP	2.0 A	Op_point	Iout operating point
20.	Phase Marg	63.204 deg	Op_point	Bode Plot Phase Margin
21.	VIN_OP	38.0 V	Op_point	Vin operating point
22.	Vout p-p	4.307 mV	Op_point	Peak-to-peak output ripple voltage
23.	Cin Pd	1.493 mW	Power	Input capacitor power dissipation
24.	Cout Pd	80.024 μW	Power	Output capacitor power dissipation
25.	Diode Pd	1.136 W	Power	Diode power dissipation
26.	IC Pd	503.783 mW	Power	IC power dissipation
27.	L Pd	286.0 mW	Power	Inductor power dissipation
28.	Total Pd	1.927 W	Power	Total Power Dissipation
29.	Vout Tolerance	2.904 %		Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable

Design Inputs

#	Name	Value	Description
1.	Iout	2.0	Maximum Output Current
2.	VinMax	38.0	Maximum input voltage
3.	VinMin	36.0	Minimum input voltage
4.	Vout	12.0	Output Voltage
5.	base_pn	TPS54340-Q1	Base Product Number
6.	source	DC	Input Source Type
7.	Ta	30.0	Ambient temperature

Design Assistance

1. The TPS54340-Q1 is qualified for Automotive applications. All passives and other components selected in this design may not be qualified for Automotive applications. The user is required to verify that all components in the design meet the qualification and safety requirements for their specific application

2. **TPS54340-Q1** Product Folder : <http://www.ti.com/product/TPS54340%2DQ1> : contains the data sheet and other resources.

Texas Instruments' WEBENCH simulation tools attempt to recreate the performance of a substantially equivalent physical implementation of the design. Simulations are created using Texas Instruments' published specifications as well as the published specifications of other device manufacturers. While Texas Instruments does update this information periodically, this information may not be current at the time the simulation is built. Texas Instruments does not warrant the accuracy or completeness of the specifications or any information contained therein. Texas Instruments does not warrant that any designs or recommended parts will meet the specifications you entered, will be suitable for your application or fit for any particular purpose, or will operate as shown in the simulation in a physical implementation. Texas Instruments does not warrant that the designs are production worthy.

You should completely validate and test your design implementation to confirm the system functionality for your application prior to production.

Use of Texas Instruments' WEBENCH simulation tools is subject to [Texas Instruments' Site Terms and Conditions of Use](#). Prototype boards based on WEBENCH created designs are provided AS IS without warranty of any kind for evaluation and testing purposes and are subject to the terms of the [Evaluation License Agreement](#).